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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Isabelle Claverie  
Serial No: 09/625,116  
Confirmation No: 7278  
Filed: July 25, 2000  
For: PROTECTION OF THE LOGIC WELL OF A COMPONENT  
INCLUDING AN INTEGRATED MOS POWER TRANSISTOR

Examiner: BENENSON, Boris  
Art Unit: 2836

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**CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)**

The undersigned hereby certifies that this document is being placed in the United States mail with first-class postage attached, addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on December 19, 2003.

  
Eileen MacKenzie

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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**AMENDMENT**

Sir:

In response to the Office Action mailed August 27, 2003, please amend the above-identified application as follows:

**Amendments to the Claims** are reflected in the listing of claims that begins on page 2 of this amendment.

**Remarks** begin on page 5 of this amendment.

